

**METHOD FOR PATCHING ROM INSTRUCTIONS IN AN ELECTRONIC
EMBEDDED SYSTEM INCLUDING AT LEAST A FURTHER MEMORY
PORTION**

Abstract of the Disclosure

A mechanism for patching read only memory (ROM) instructions in an embedded system is provided. The embedded system includes non-volatile memory such as a ROM, a RAM and an EEPROM. In one configuration, the ROM contains application modules (subroutines) that are subordinate to a flag status. The flag status indicates to the subroutine whether to execute the ROM instructions in the ROM or the extended instructions in the EEPROM. The instructions in the EEPROM may be used to patch or to extend the behaviors masked in the ROM instructions. The extended instructions in the EEPROM may reuse the ROM instructions in the ROM without resulting in recursive actions.